

AMENDMENTS TO THE CLAIMS

Sub 1
Claim 1. (Previously Amended) A wireless communication terminal comprising:
wireless transmitting/receiving means for transmitting and receiving data;
detecting means for detecting a strength of a receiving electric field by said wireless transmitting/receiving means; and
operation clock control means for controlling, based on said receiving electric field strength detected by said detecting means, a frequency of an operation clock used for processing data transmitted and received by said wireless transmitting/receiving means.

Claim 2. (Previously Amended) The wireless communication terminal according to claim 1, said operation clock control means including means for reducing said frequency of said operation clock as said strength of said receiving electric field decreases.

Claim 3. (Previously Amended) The wireless communication terminal according to claim 1, wherein:
said detecting means includes memory means for storing a measured value of said strength of said receiving electric field; and
said operation clock control means includes means for controlling said frequency of said operation clock based on said measured value stored by said memory means.

Claim 4. (Previously Amended) The wireless communication terminal according to claim 1, wherein:
said detecting means includes means for selecting and maintaining a receiving level inferential value from a plurality of previously stored receiving level inferential values, wherein said selected receiving level inferential value corresponds to said detected receiving electric field; and
said operation clock control means includes:

a plurality of operation clock generation means that generate operation clocks corresponding to said receiving level inferential values; and

selecting means for selecting one of said plurality of operation clock generation means corresponding to said receiving level inferential value that is maintained by said detecting means.

Claim 5. (Previously Amended) The wireless communication terminal according to claim 1, wherein:

said wireless transmitting/receiving means performs transmitting/receiving processing in accordance with a Time Division Multiple Access (TDMA) communication method; and

said operation clock control means controls said frequency of said operation clock in synchronization with a timing of a time division receiving operation of said wireless transmitting/receiving section.

Claim 6. (Previously Amended) The wireless communication terminal according to said claim 5:

said wireless transmitting/receiving means including means for generating an interrupt signal at a starting time of a receiving slot of said time division receiving operation and an interrupt end signal at an ending time of said receiving slot; and

said operation clock control means including means for controlling said operation clock frequency according to said receiving electric field strength in response to said interrupt signal, and means for ending said control of said operation clock frequency in response to said interrupt end signal.

Claim 7. (Original) A wireless communication terminal comprising:
wireless transmitting/receiving means generating an interrupt signal at a starting time of a receiving slot that is a timing of time division receiving operation and an interrupt end signal at an ending time of said receiving slot as well as transmitting and receiving data;

detecting means detecting a receiving electric field strength at said wireless transmitting/receiving means and selecting said receiving level inferential value corresponding to said detected receiving electric field strength from a predetermined number of receiving level inferential values stored in advance and maintaining said receiving level inferential value;

a predetermined number of operation clock generation means generating operation clocks corresponding to said receiving level inferential values;

selecting means selecting operation clock generation means corresponding to a receiving level inferential value maintained at said detecting means out of said predetermined number of operation clock generation means in response to said interrupt signal and selecting an initially selected operation clock generation means in response to said interrupt end signal; and

data processing means processing data transmitted and received by said wireless transmitting/receiving means based on an operation clock of operation clock generation means selected by said selecting means.

Claim 8. (Previously Amended) In a wireless communication transceiver, a method of controlling an operation clock for processing transmitted/received data, said method comprising the steps of:

detecting a strength of a receiving electric field; and

controlling a frequency of said operation clock based on said detected strength of said receiving electric field.

Claim 9. (Previously Amended) The method according to claim 8, wherein said controlling step includes a step of controlling said frequency of said operation clock by reducing said frequency of said operation clock as said detected strength of said receiving electric field decreases.

Claim 10. (Previously Amended) The method according to claim 8, wherein: said detecting step includes a step of selecting, out of said plurality of receiving level inferential values, a receiving level inferential value corresponding to said detected strength of said receiving electric field; and

said control step includes a step of selecting, out of said plurality of operation clocks, an operation clock corresponding to said selected receiving level inferential value.

Claim 11. (Currently Amended) The method according to claim 8, wherein:
said processing of said transmitted/received data is performed in accordance with a Time Division ~~Multiple~~ Multiple Access (TDMA) communication method; and
said controlling step includes a step of controlling said frequency of said operation clock in synchronization with a timing of a time division receiving operation.

Claim 12. (Previously Amended) The method according to claim 11, wherein
said controlling step includes a step of starting to control said frequency of said operation clock at a starting time of a receiving slot and ending said control of said frequency of said operation clock at an ending time of said receiving slot.

Claim 13. (Previously Amended) The method according to claim 12, wherein
said controlling step includes a step of returning said frequency of said operation clock to an original frequency of said operation clock at said ending time of said receiving slot.

Claim 14. (Previously Added) A wireless communication terminal comprising:
a wireless transceiver that transmits and receives data;
an electric field detector that detects a strength of an electric field received by the wireless transceiver;
an operation clock; and
a control circuit that changes the frequency of the operation clock based on the detected strength of the received electric field.

Claim 15. (Previously Added) A wireless communication terminal according to claim 14, further comprising a central processing unit (CPU), wherein an operating speed of the CPU changes according to the frequency of the operation clock.

Claim 16. (Previously Amended) A method for controlling a frequency of an operation clock in a wireless communication transceiver, the method comprising:

detecting a strength of an electric field received by the wireless communication transceiver; and

changing the frequency of the operation clock based on the detected strength of the received electric field.

Amended

Claim 17. (Previously Added) The method according to claim 16, further comprising changing an operating speed of a central processing unit in accordance with the frequency of the operation clock.
